



AT9-98-544

PATENT

- 1 -

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Balaram Sinharoy
Serial No.: 09/435,070
Filed: November 4, 1999
Title: CIRCUITS, SYSTEMS AND
METHODS FOR PERFORMING
BRANCH PREDICTIONS BY
SELECTIVELY ACCESSING BIMODAL
AND FETCH-BASED BRANCH
HISTORY TABLES

Before the Examiner:
William Wood
Group Art Unit: 2124
IBM Corporation
Intellectual Property Law
11400 Burnet Road
Austin, Texas 78758

*#14/0
S. Celler
3/15/04*

RESPONSE TO NOTICE OF NON-COMPLIANCE UNDER 37 C.F.R. § 1.121

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

RECEIVED

MAR 09 2004

Technology Center 2100

Dear Sir:

In response to the Notice of Non-Compliant Amendment having a mailing date of February 13, 2004 (Paper No. 13), with a one-month shortened statutory period for response set to expire on March 13, 2004, Applicant submits herewith a complete listing of all the claims:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on March 1, 2004.

Serena Beller
Signature

Serena Beller
(Printed name of person certifying)